

1/10

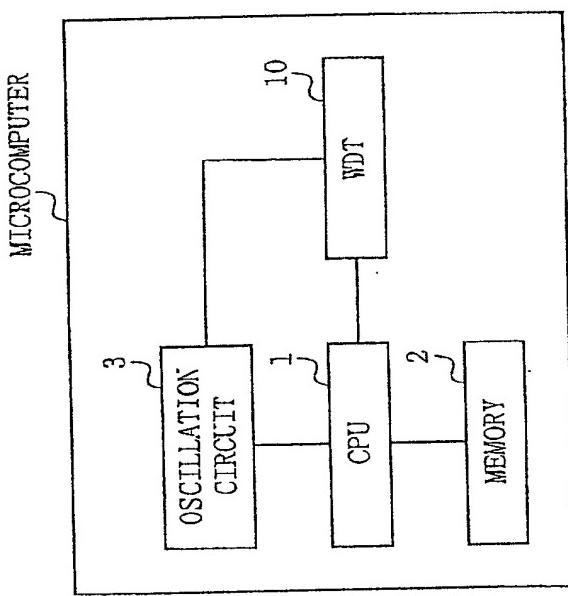


FIG. 1

2/10

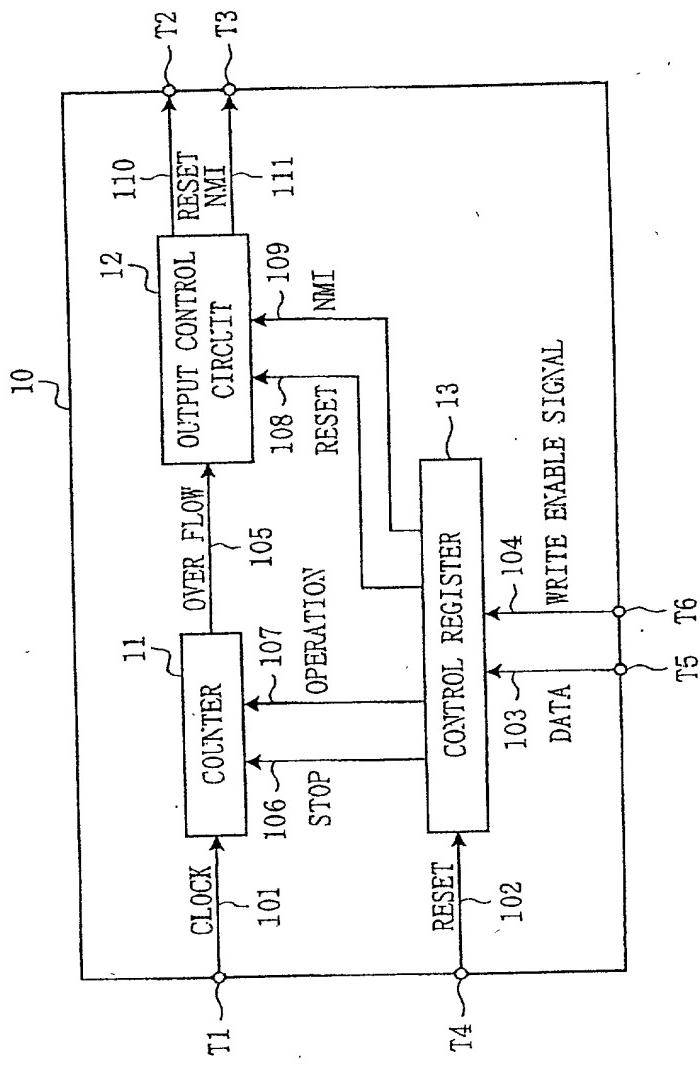


FIG. 2

3/10

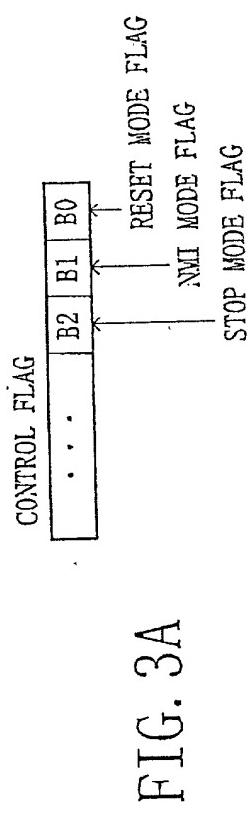


FIG. 3B

OPERATIONAL MODE	B2	B1	B0
RESET MODE	X	X	0
NMI MODE	X	0	1
STOP MODE	0	1	1

X : DON'T CARE

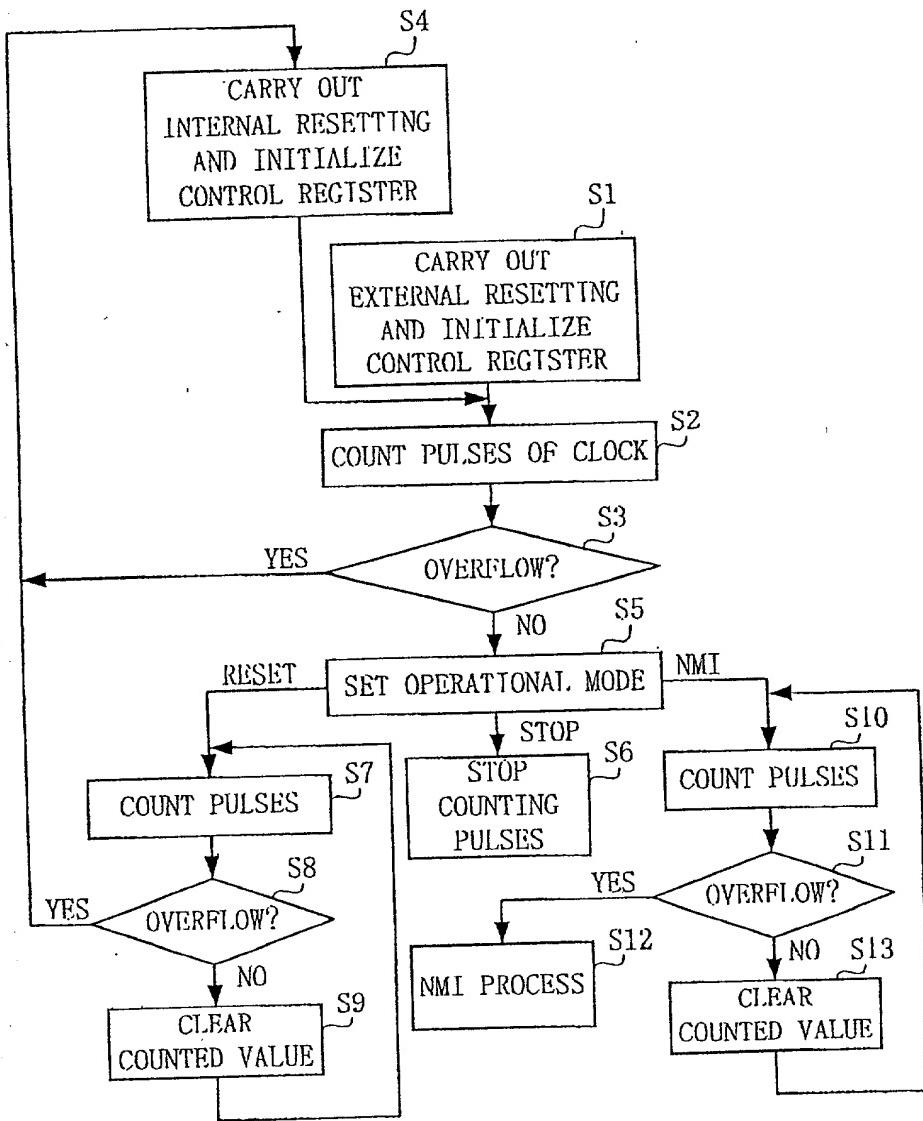


FIG. 4

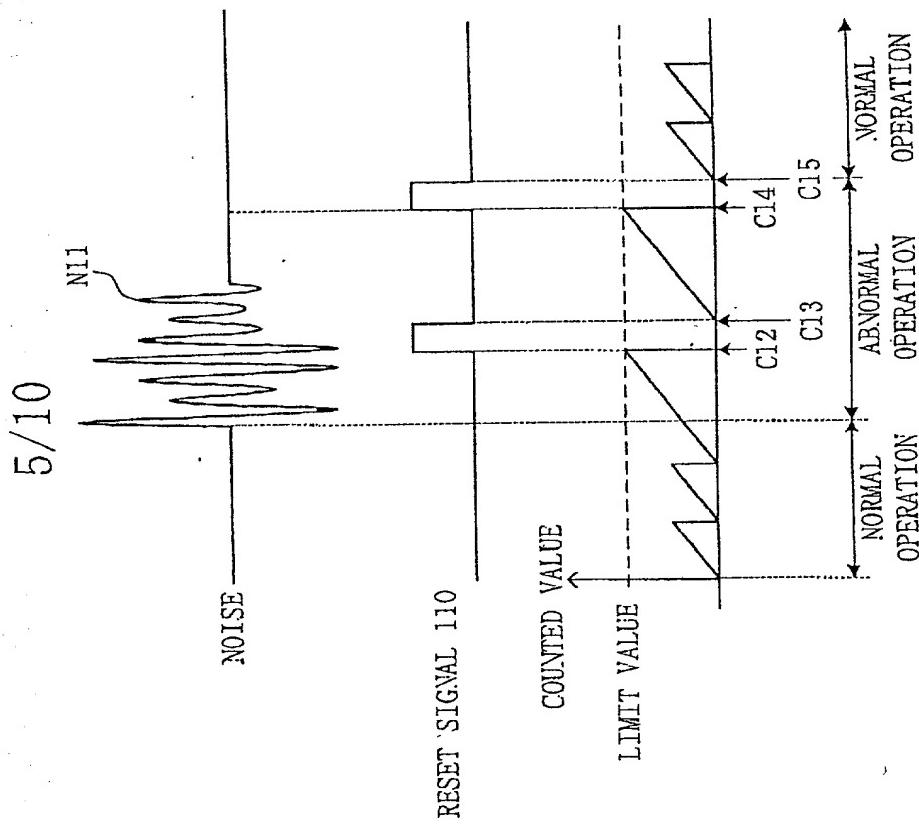


FIG. 5

6/10

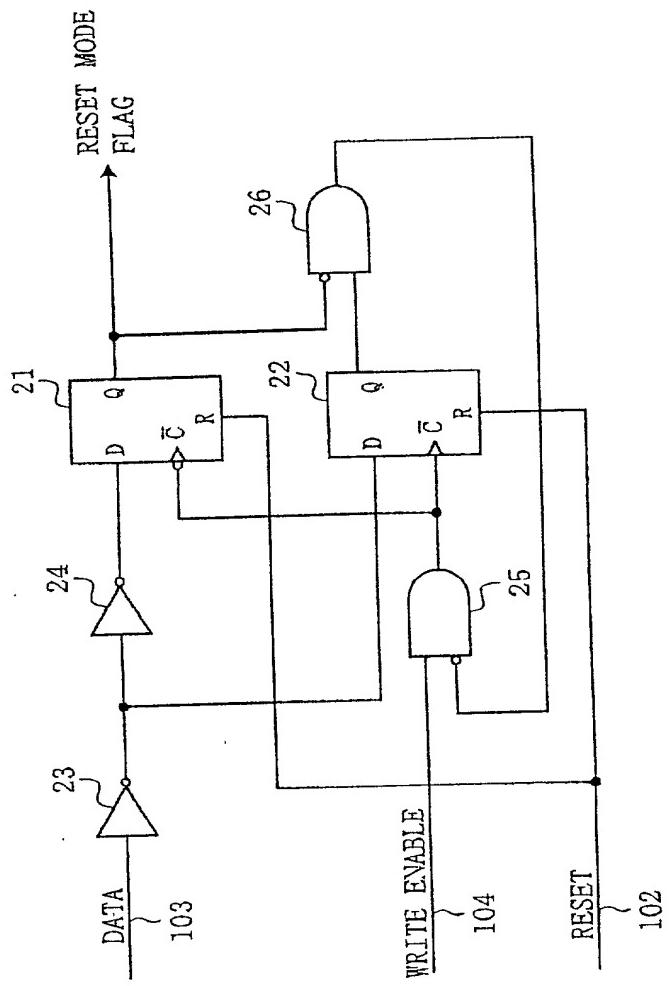
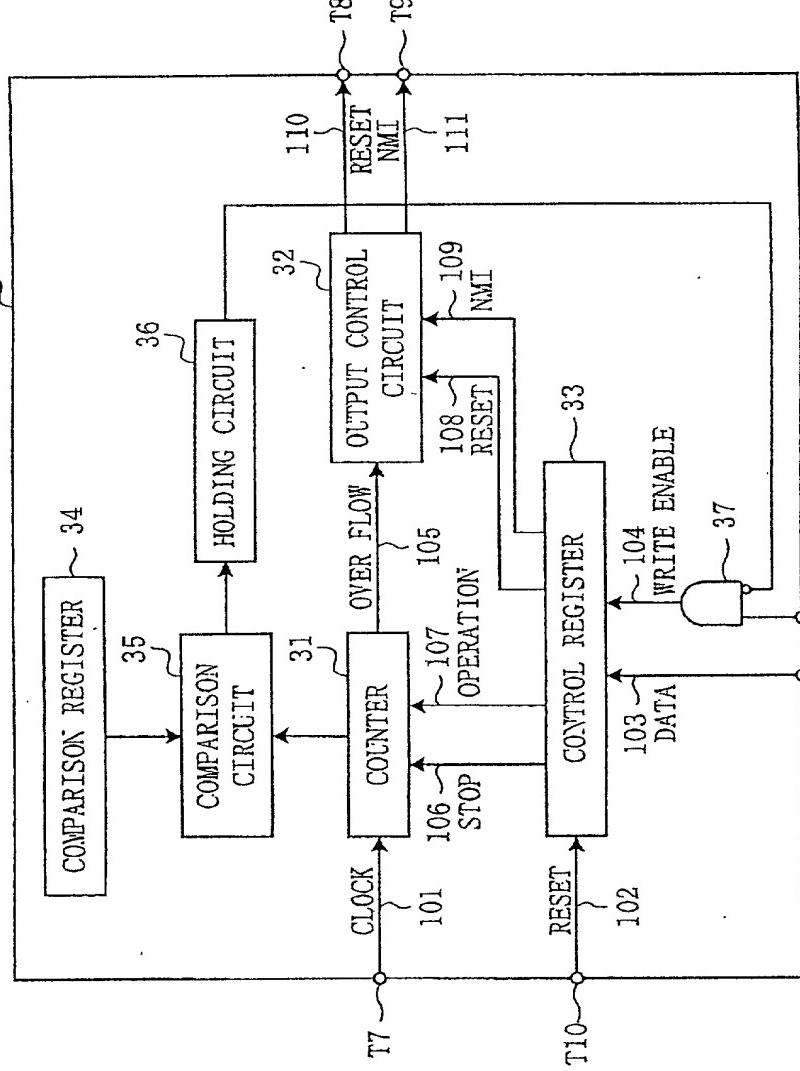


FIG. 6

7/10 30



T11 T12 FIG. 7

8/10

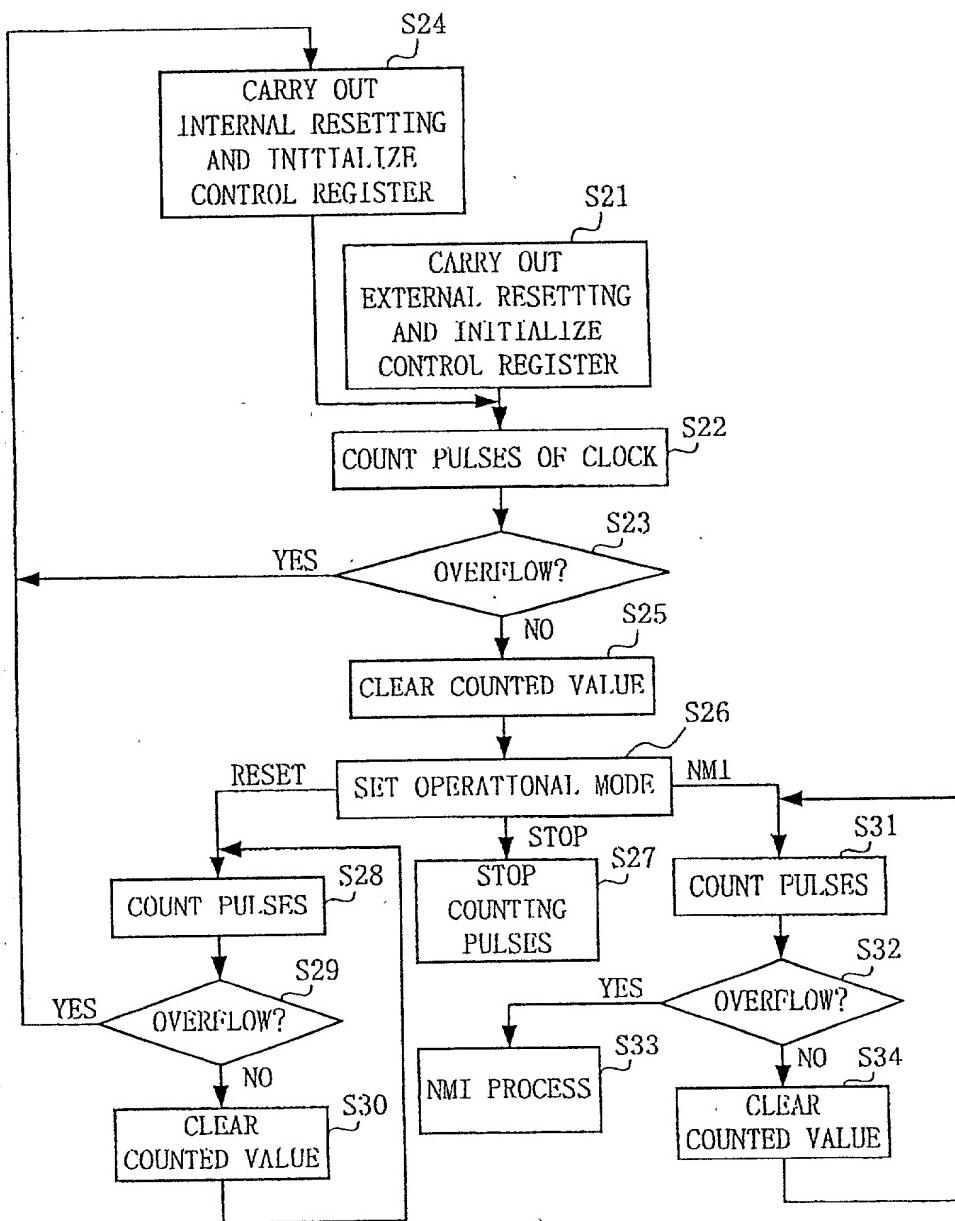


FIG. 8

9/10

	ABNORMAL OPERATION	FACTOR
A	<ul style="list-style-type: none">• TRANSISTOR (T_F) DOES NOT OPERATE• CLOCK FOR WDT IS NOT PROVIDED	<ul style="list-style-type: none">• BREAKAGE OF POWER SOURCE LINE• BREAKAGE OF OSCILLATOR
B	<ul style="list-style-type: none">• T_F OPERATES• CLOCK FOR WDT IS PROVIDED• SOFTWARE DOES NOT RUN	<ul style="list-style-type: none">• ABNORMAL OPERATION IN CPU, ROM, RAM, ETC. (SUCH AS VOLTAGE DROP IN CPU)
C	<ul style="list-style-type: none">• T_F OPERATES• CLOCK FOR WDT IS PROVIDED• SOFTWARE RUNS• TEMPORARY ABNORMALITY	<ul style="list-style-type: none">• COMMON NOISE (E. G. IN POWER SOURCE)• ERROR IN SOFTWARE DESIGN (E. G. UNDEFINED CONDITION)

FIG. 9

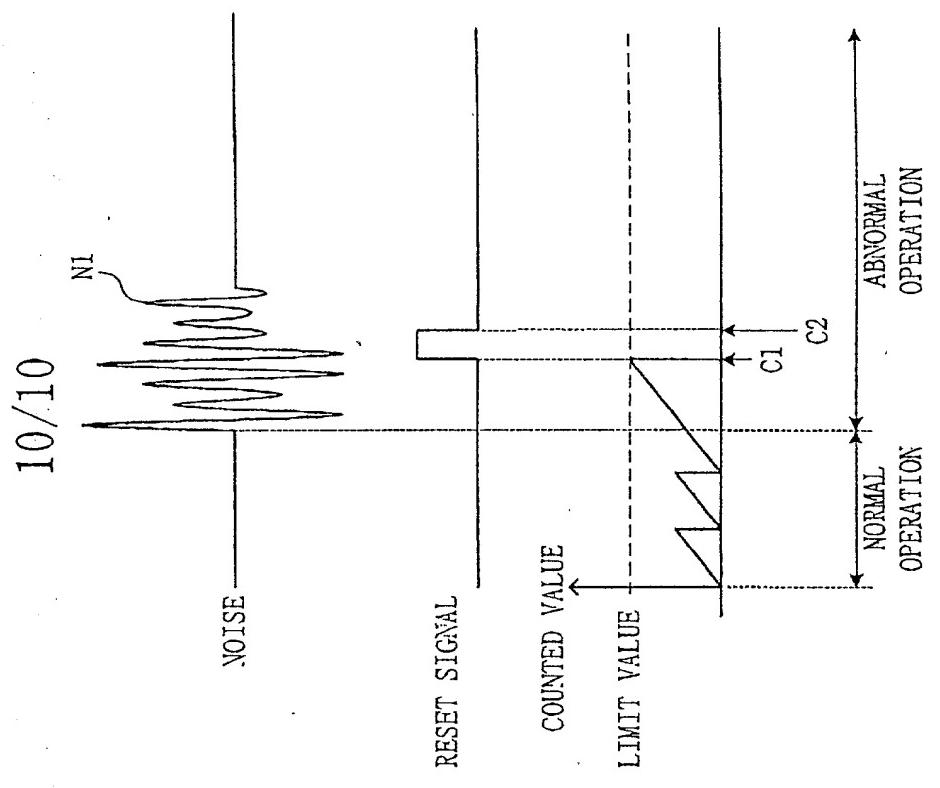


FIG. 10